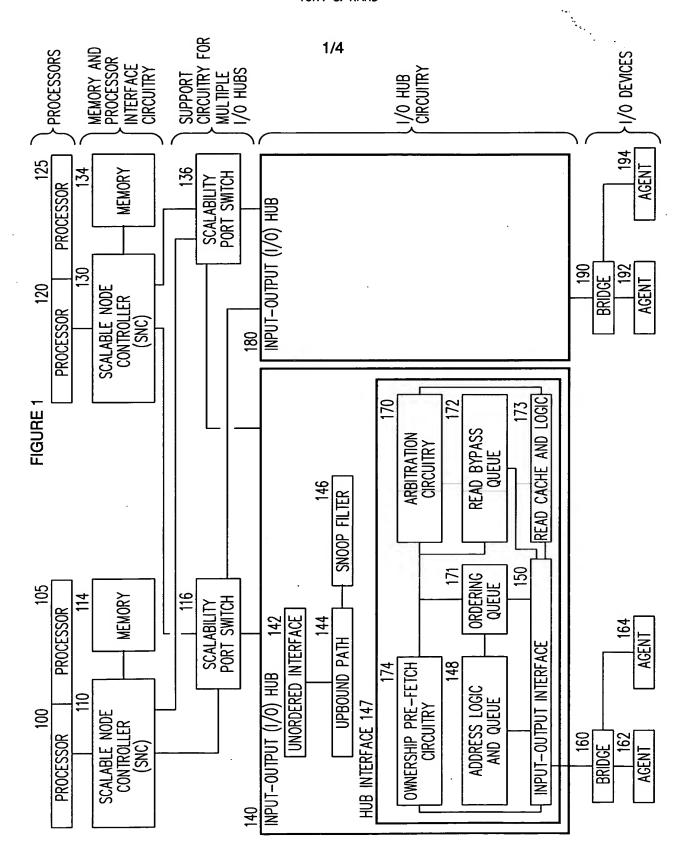
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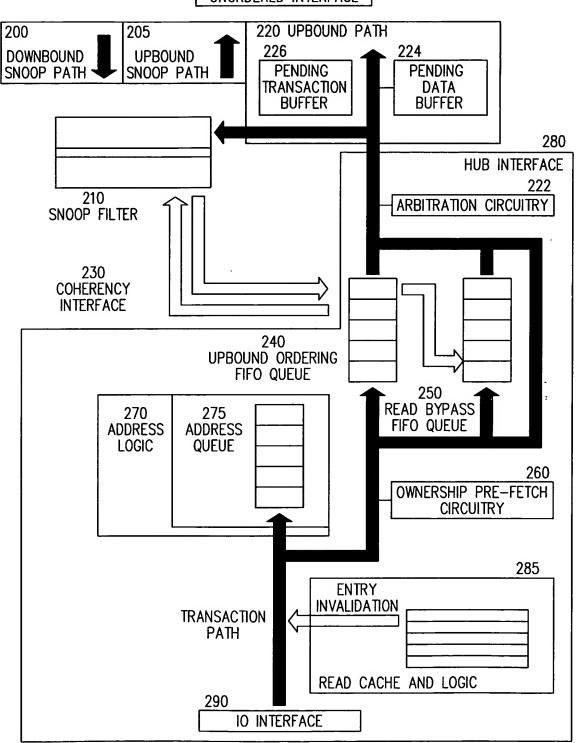
42390P12361 CACHE COHERENCY ARRANGEMENT TO ENHANCE INBOUND BANDWIDTH TONY S. RAND

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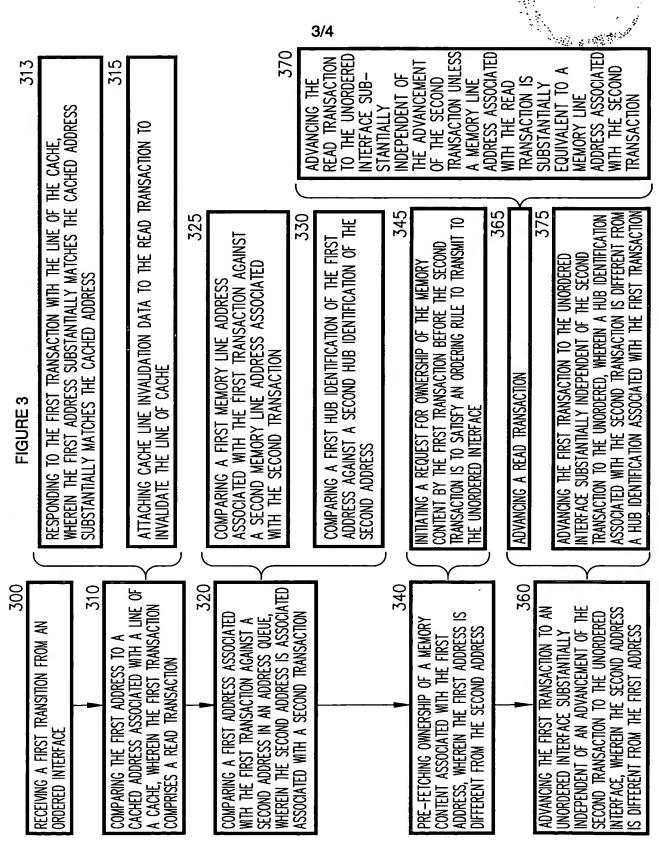


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FIGURE 2
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UNORDERED INTERFACE



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	PRE-FETCHING OWNERSHIP OF A MEMORY CONTENT ASSOCIATED WITH THE FIRST ADDRESS WHEREIN THE FIRST ADDRESS IS DIFFERENT FROM THE SECOND ADDRESS ATED	410	
420 ATED	420 ATED	• • • • • • • • • • • • • • • • • • • •	430
420 ATED	420 ATED		MEMORY FIRST ADDRESS,
		WHEREIN THE FIRST ADDRESS IS FROM THE SECOND ADDRESS	DIFFERENT
COMPARING A FIRST ADDRESS ASSOCIATED WITH THE FIRST TRANSACTION AGAINST A SECOND ADDRESS IN AN ADDRESS QUEUE, WHEREIN THE SECOND ADDRESS IS ASSOCIATED	COMPARING A FIRST ADDRESS ASSOCIATED WITH THE FIRST TRANSACTION AGAINST A SECOND ADDRESS IN AN ADDRESS QUEUE, WHEREIN THE SECOND ADDRESS IS ASSOCIATED WITH A SECOND TRANSACTION	420	
WITH THE FIRST TRANSACTION AGAINST A SECOND ADDRESS IN AN ADDRESS QUEUE, WHEREIN THE SECOND ADDRESS IS ASSOCIATED	WITH THE FIRST TRANSACTION AGAINST A SECOND ADDRESS IN AN ADDRESS QUEUE, WHEREIN THE SECOND ADDRESS IS ASSOCIATED WITH A SECOND TRANSACTION	COMPARING A FIRST ADDRESS ASSOCIATED	
WHEREIN THE SECOND ADDRESS IS ASSOCIATED WITH A SECOND TRANSACTION	WHEREIN THE SECOND ADDRESS IS ASSOCIATED WITH A SECOND TRANSACTION	WITH THE FIRST TRANSACTION AGAINST A SECOND ADDRESS IN AN ADDRESS QUEUE.	
		WHEREIN THE SECOND ADDRESS IS ASSOCIATED WITH A SECOND TRANSACTION	

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